77. (New) The method of claim 75, wherein a first well isolating the first transistor from a substrate of the integrated circuit is coupled to the first source/drain and gate of the second transistor and wherein a second well isolating the second transistor from a substrate of the integrated circuit is coupled to the at least one internal circuit.

REMARKS

Claims 1, 6 and 15 are canceled and claims 22-77 have been added. Accordantly, claims 22-77 are currently pending in the application.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 2nd day of November 2001.

Amy Moriarty
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